(FILE 'USPAT' ENTERED AT 10:07:58 ON 15 JUN 1999)
                  ACT SAVEALL/L

L1 (705)SEA FILE=USPAT (ON-CHIP OR ON CHIP)(1W) PROGRAM####
L2 (50810)SEA FILE=USPAT MICROCOMPUTER# OR MICROCONTROLLER#
L3 (168)SEA FILE=USPAT L1(1P) L2
L4 (361)SEA FILE=USPAT EMBED#### (2W) MEMORY
L5 (1)SEA FILE=USPAT L3 (1P) L4
L6 (160)SEA FILE=USPAT (ON-CHIP OR ON CHIP) (1W) PROGRAMMING
L7 (0)SEA FILE=USPAT L6 (1P) L4
L8 (78531)SEA FILE=USPAT FLASH OR EEPROM
L9 (36)SEA FILE=USPAT L6 (1P) L8
L10 (36)FOC L9 1-
L11 (607)SEA FILE=USPAT LOAD### BLOCK
L12 (36)SEA FILE=USPAT L10
L13 (0)SEA FILE=USPAT L12 AND L11
L14 (0)SEA FILE=USPAT L6 AND L11
L15 (44)SEA FILE=USPAT L8 AND L11
L16 (44)FOC L15 1-
L17 (8)SEA FILE=USPAT L6 (1P) L2
L18 (8)FOC L17 1-
L19 (1)SEA FILE=USPAT ((ON-CHIP/TI OR ON CHIP/TI) (1W) PROGRAMMIN
G/T
L20 (69)SEA FILE=USPAT 712/37/CCLS
L21 (1)SEA FILE=USPAT L20 AND L6
L22 (1235)SEA FILE=USPAT (IN-SYSTEM OR IN SYSTEM)(1W) PROGRAMMING
L23 (76)SEA FILE=USPAT L2 (1P) L22
L24 (6)SEA FILE=USPAT L23 (1P) L8
L25 (6)FOC L24 1-

L26   108 S SELF-PROGRAMMING OR (SELF PROGRAMMING)
L27   1498 S L6 OR L22 OR L26
L28   3578 S (ON-CHIP OR ON CHIP)(2W)MEMORY
L29   9048 S (PARTITION#### OR DIVID###)(3A)(MEMORY OR EEPROM OR EP
ROM
L30 ·   637 S L28 AND L29
L31   135 S L28 (1P) L29
L32   135 FOCUS L31 1-
L33   578338 S (PARTITION#### OR DIVID###)
L34   417 S L28(1P)L33
L35   41 S L34(1P)L8
L36   41 FOCUS L35 1-
L37   59 S L34 (1P)L2
L38   59 FOCUS L37 1-

                     El-Hady, N.

1. 5,857,939, Jan. 12, 1999, Exercise device with audible electronic monitor; Arthur H. Kaufman, 482/8; 73/379.01; 482/1, 9, 902; 601/23 [IMAGE AVAILABLE]

2. 5,831,515, Nov. 3, 1998, Electronic siren apparatus including an integrated handheld microphone and control handle; John Stewart, et al., 340/384.4, 384.5, 384.6, 384.7, 471 [IMAGE AVAILABLE]

3. 5,675,762, Oct. 7, 1997, System for locking down part of portion of memory and updating page directory with entry corresponding to part of portion of the memory locked down; William Kress Bodin, et al., 711/206 [IMAGE AVAILABLE]

4. 5,481,253, Jan. 2, 1996, Automotive security system; Michael D. Phelan, et al., 340/825.31; 307/10.2, 10.4, 10.6 [IMAGE AVAILABLE]

5. 5,473,758, Dec. 5, 1995, System having input output pins shifting between programming mode and normal mode to program memory without dedicating input output pins for programming mode; Ray Allen, et al., 711/103; 364/232.9, 244.6, DIG.1; 365/52; 708/231; 710/14 [IMAGE AVAILABLE]

6. 5,426,759, Jun. 20, 1995, On-chip/off-chip memory switching using system configuration bit; Ajay J. Padgaonkar, 711/1; 364/243, 246, 246.11, DIG.1 [IMAGE AVAILABLE]

7. 5,402,014, Mar. 28, 1995, Peripheral port with volatile and non-volatile configuration; Arye Ziklik, et al., 326/37, 38, 40; 327/407; 365/189.08 [IMAGE AVAILABLE]

8. 4,751,570, Jun. 14, 1988, Generation of apparently three-dimensional images; Max Robinson, 348/47, 213, 335; 352/65 [IMAGE AVAILABLE]

El-Hady, N.